



THE IDS3102-MCT LWIR IRFPA SYSTEM

The IDS3102 IRFPA Imager System consists of the LN₂ pour-fill cryostat, rear-mounted electronics assembly, and external power supply. The system is intended to serve as a development tool for engineers and scientists interested in evaluating sensors or developing IRFPA applications. The system also serves as a standalone IRFPA camera system for dedicated installations, using the p1394a (FireWire™) interface available on many computers. For spectrometer applications, the system can utilize the Talktronics DTSpec Imaging Spectrometer system software and external user-defined optics for a complete spectrometer system.

The electronics module contains the digital timing controller that generates all clock signals and bias voltages for the IRFPA, digitizes the pixels and sends the digitized pixel information to a computer (PC or other) via the p1394a (FireWire™) Digital Camera Interface.

The IRFPA is a LWIR HgCdTe (MCT) focal plane array, with 30x30 μm pixels in 320x240 format that can be expanded to 384x288. The sensor is a staring type device with selectable gain, frame size and integration time. Sensitivity of the device spans approximately 2 μm to 10 μm.

The electronics module can be controlled via software in the p1394a Camera Setup software control panel, or alternatively can be commanded via the RS232C serial port. Pixel correction is provided via two-point correction method, with a gain map downloaded via the RS232C serial port, and stored in the camera while power is maintained. The Camera Setup software provides global control of analog gain (via FPA), digital gain (via pixel processor), offset, automatic background subtraction mode, integration time and image capture functions. A trigger output is provided for synchronizing external hardware (TTL pulse).

The IDS3102 IRFPA Camera Dewar and electronics assembly may be remotely located from the PC (standard cable is 6 feet), and the power supply (6 ft. cable).

LN₂ CRYOSTAT

The IRFPA sensor is enclosed in an LN₂-cooled Cryostat. Breakdown of the Cryostat for access to the device is simplified, suitable for rapid disassembly for test purposes.

Cool-down time from room temperature to <80° K is typically about thirty-five minutes. Hold time with <100 mW device heat load is greater than 15 hours.

The window is nominally 50mm X 3mm, and is replaceable. The standard window supplied for LWIR applications is A/R coated ZnSe. A/R coated Ge is available as an option. Provision is made in the cold shield and sensor clamp for a standard-sized (25-25.4mm) "cold filter" (not included, optional order item).

Automatic LN₂ filling kits are available that include an LN₂ transfer line, valve, and thermal controller for use with pressurized LN₂ storage vessels.

IDS-3102-MCT IRFPA CAMERA SYSTEM SPECIFICATIONS

IRFPA	Material	HgCdTe	Camera Control	<ul style="list-style-type: none"> ● Input Offset (DAC) ● ADC Offset (DAC) ● Brightness ● Contrast ● Global Digital Gain ● Background collection ● Integration time
	Format	320x240 nominal 384x288 max		
	Pixel size	30x30 μm		
	ROIC noise	< 150 μV		
	ROIC output voltage range	2 V typ		
	Spectral response	7.5 – 10.5 μm (measured) 2 – 10.5 μm typical		
	QE	> 0.45 typical		
	Capacity	>29 Me-		
FPA	Signal Processing		RS232C Interface	All of above, plus:
	Preamp	2 channels		<ul style="list-style-type: none"> ● Debug features ● RAM test ● Temperature display ● Gain map calibration ● Gain map upload/download ● Image RAM download
	Gain Adjustment	Hardware gain is controlled via software		
ADC	Resolution	Dual 14 bits	Power Supply (External)	85 – 240 VAC 50/60 Hz
	Sample Rate	2 MHz		
	SNR	>70 dB	Input	200 VA
	INL	± 1 LSB	Power Consumption	12x8x3 inches
	DNL	$\pm 3/4$ LSB	Size	5 lbs
	Noise	< 1 DN, input shorted	Weight	
	Non-uniformity correction	Per-pixel DC offset Per-pixel gain		
	Input signal range	$\pm 1.25\text{V}$		
System	Analog Gain, per channel	1.5 to 150 V/V (fixed resistors)	LN2 Dewar (including electronics)	12"x8"x13" HxWxD
	Digital Gain	0.5 to 2	Size	30x20x33 cm
	Sensitivity	3 μV min	Weight	20 lbs
	Noise	< 5 DN rms at 2 MHz	Vacuum port	Cryolab SV8 ISO DN16 flange or 1/2" tube
	Integration control	<100 μsec to 16 msec typ	LN2 capacity	1 liter
	Background subtraction	Performed in real time		
	Gain correction	Performed in real time		
	Frame buffer	Dual image buffer		
	Pixel clock rate	1 – 2 MHz typ		
	Frame rate	15 fps typical		
Interface	P1394a (FireWire)	6 pin (power not used)		
	RS232C	9 pin "D"		
Software	P1394a Image Capture	Image capture to disk ".avi" movie or ".bmp" snapshot files		

FireWire™ is a trademark of Apple Computer

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